Docket No.: M4065.0646/P646

Inventor: Howard E. Rhodes

Title: OPTIMIZED TRANSISTOR FOR IMAGER DEVICE

REPLACEMENT SHEET



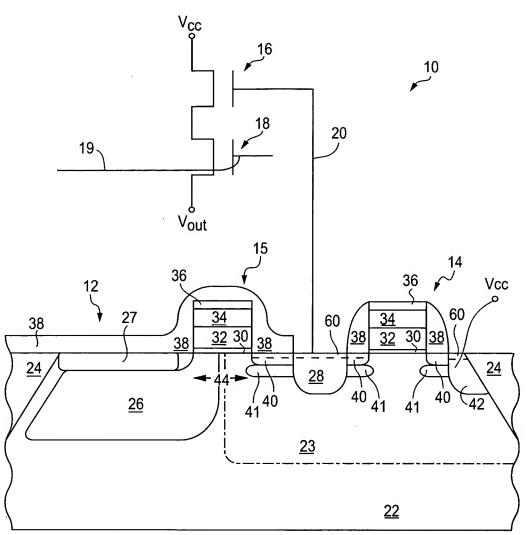


FIG. 1(a)

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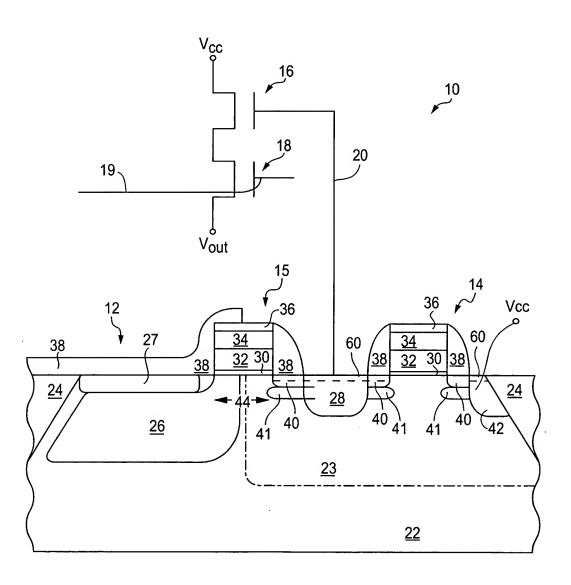


FIG. 1(b)

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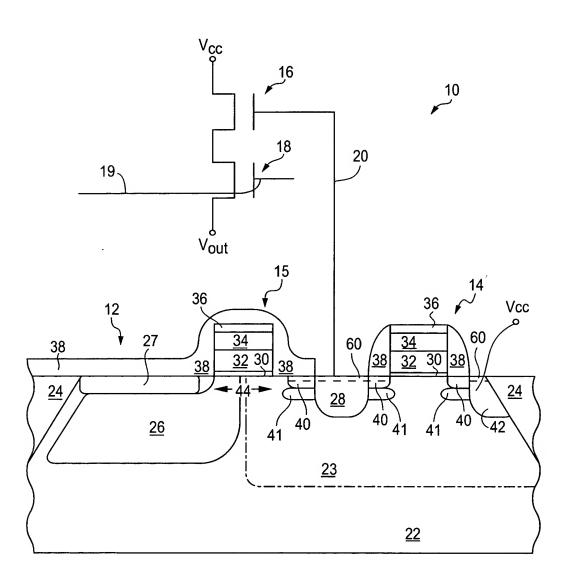


FIG. 1(c)

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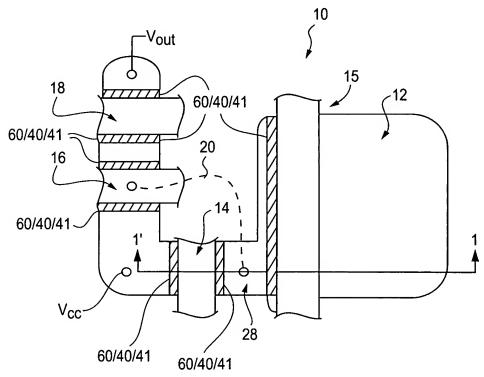


FIG. 2(a)

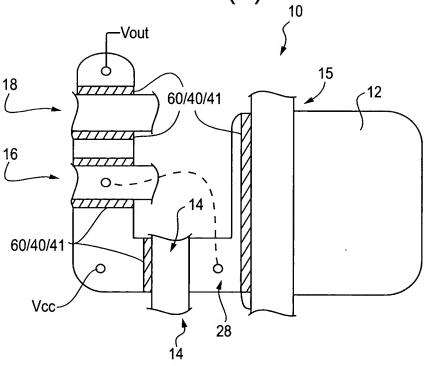


FIG. 2(b)

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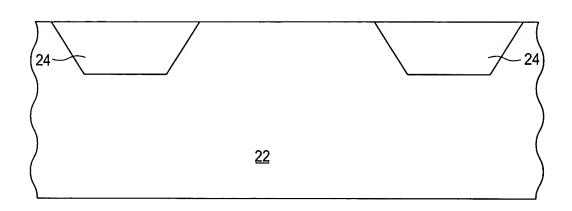


FIG. 3

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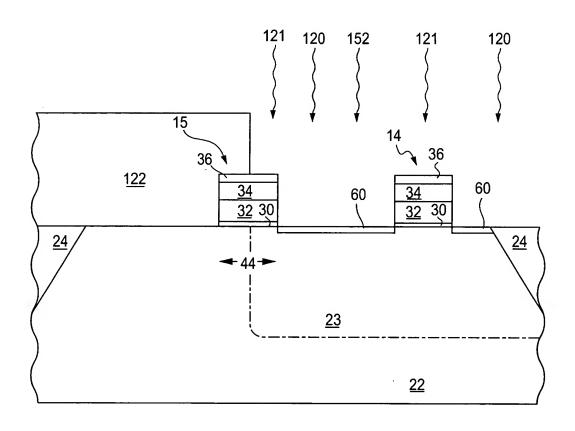


FIG. 4(a)

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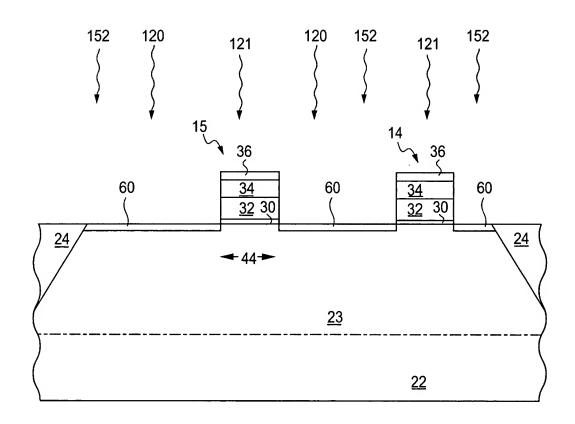


FIG. 4(b)

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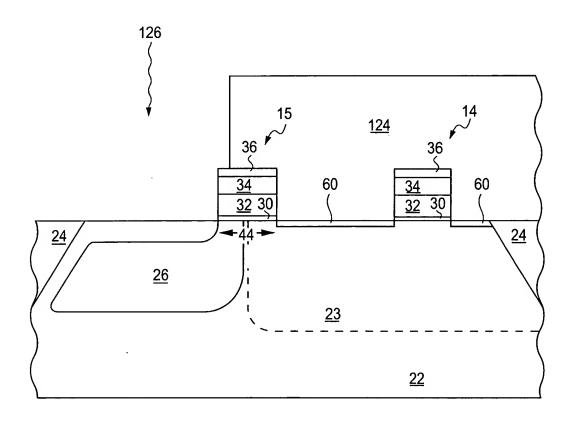


FIG. 5

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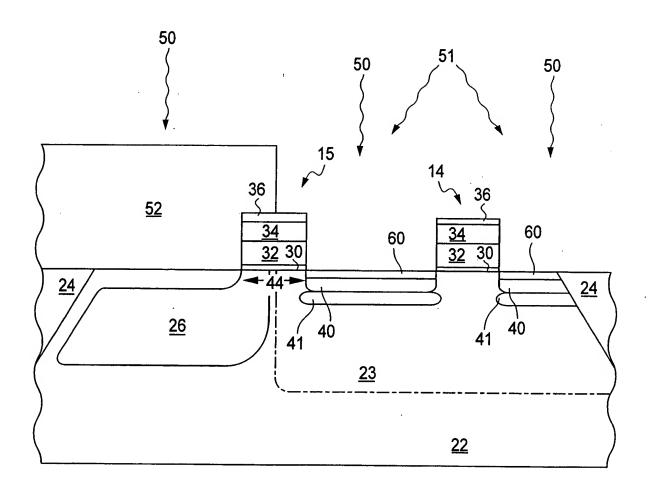


FIG. 6(a)

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REPLACEMENT SHEET

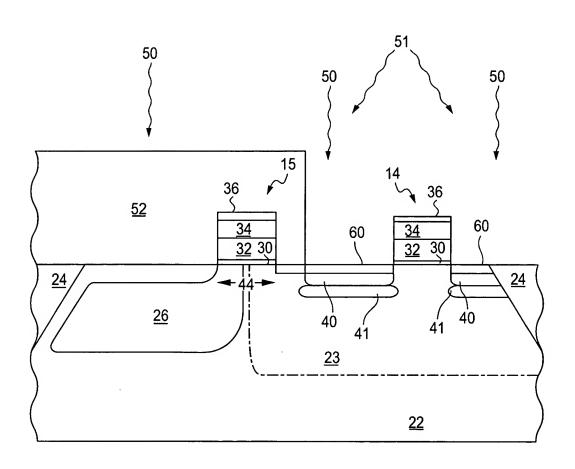


FIG. 6(b)

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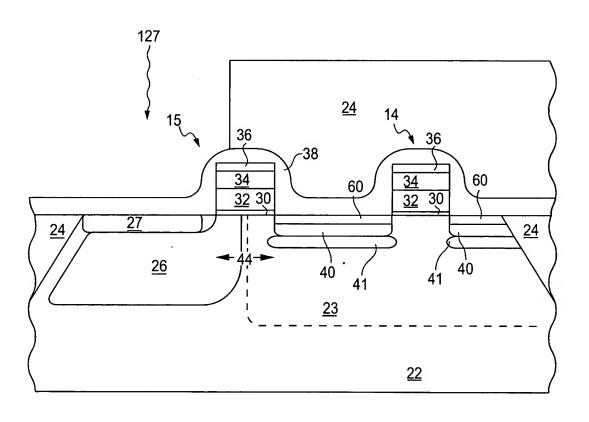


FIG. 7(a)

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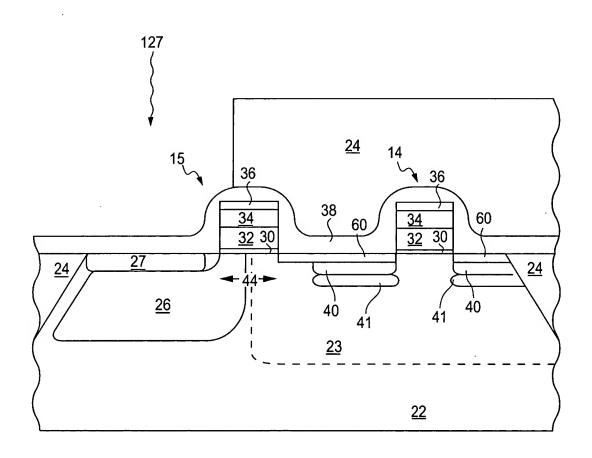


FIG. 7(b)

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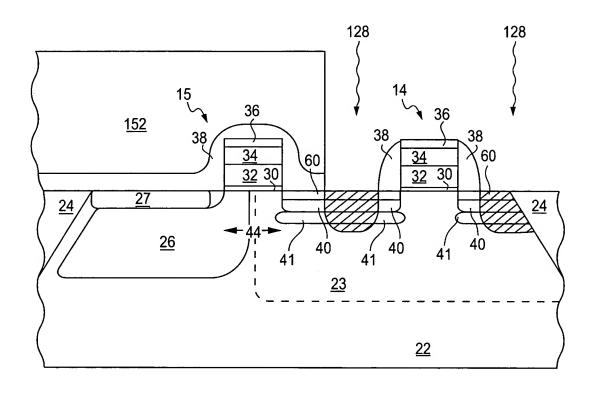


FIG. 8(a)

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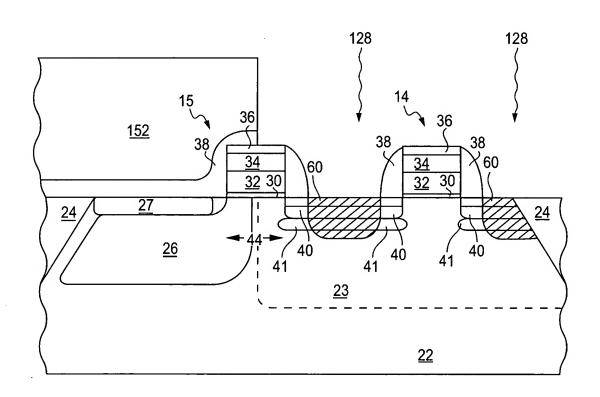


FIG. 8(b)

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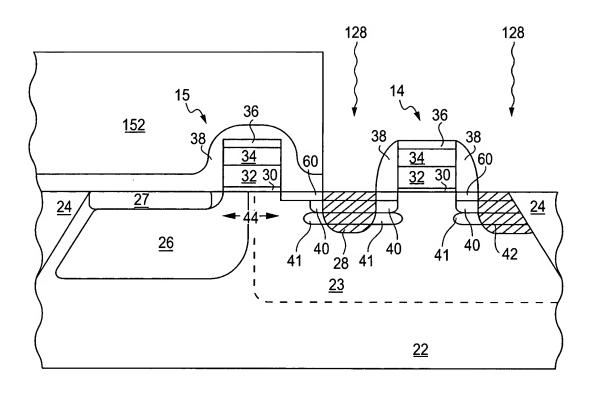


FIG. 8(c)

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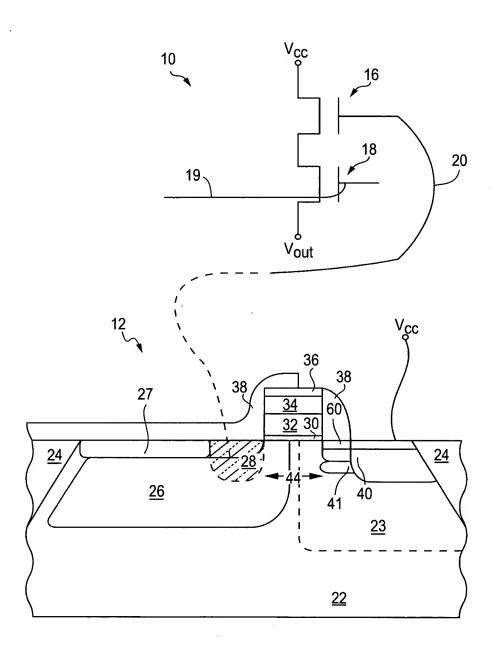


FIG. 9

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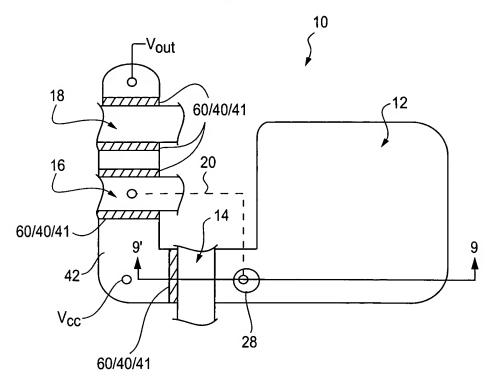


FIG. 10a

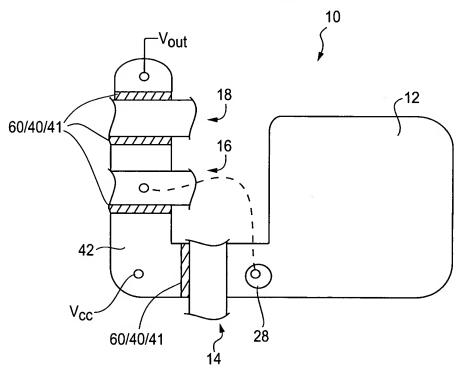


FIG. 10b

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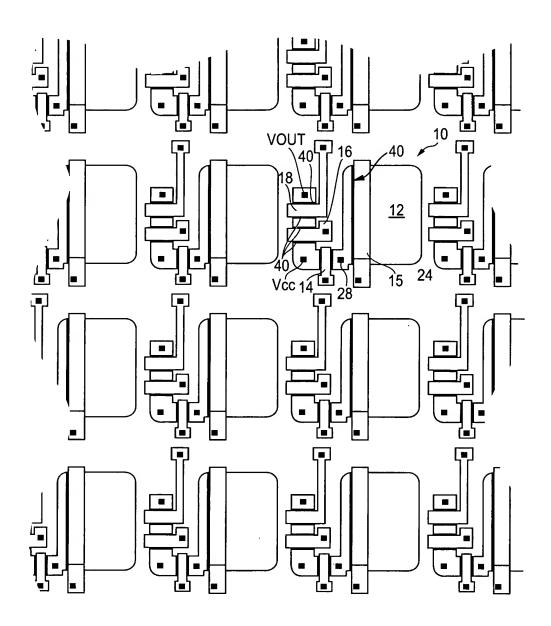


FIG. 11

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REPLACEMENT SHEET

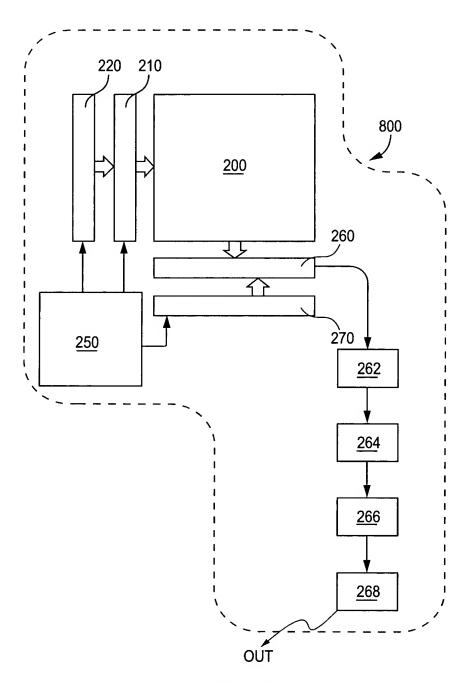
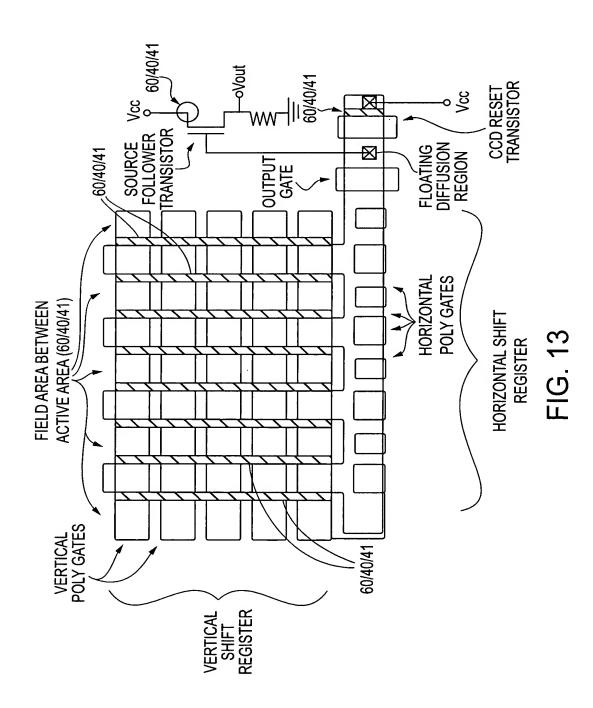


FIG. 12

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Title: OPTIMIZED TRANSISTOR FOR IMAGER DEVICE

REPLACEMENT SHEET



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Inventor: Howard E. Rhodes

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